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Optimizing an ANSI C interpreter with superoperators - ► [uvic.ca \(pdf\)](#)

TA Proebsting - Proceedings of the 22nd ACM SIGPLAN-SIGACT symposium on ... 1995 - [portal.acm.org](#)

... Implementation of a hybrid translator/interpreter that emulates ... Compilers typically translate source code into ... machine's instruction set, but an interpreter writes ...

Cited by 111 - Related articles - Web Search - BL Direct - All 12 versions

[book] Efficient implementation of the smalltalk-80 system

LP Deutsch, AM Schiffman - 1984 - ACM New York, NY, USA

Cited by 362 - Related articles - Web Search - Library Search - All 6 versions

... and method of minimizing performance degradation of an instruction set translator due to self- ...

JS Langford - US Patent 5,875,318, 1999 - Google Patents

... such an extent that it becomes as slow as, or slower than, an instruction set interpreter. ... The instruction set translator will first translate blocks of ...

Cited by 7 - Related articles - Web Search - All 3 versions

PA-RISC to IA-64: Transparent Execution, No Recompile - ► [hp.com \(pdf\)](#)

C Zheng, C Thompson - 2000 - [doi.ieeecomputersociety.org](#)

... This combination also provides instruction set architecture (ISA) emulation ... executed code into native IA-64 instructions. ... make it easy to translate PA-RISC ...

Cited by 67 - Related articles - Web Search - BL Direct - All 11 versions

Method for providing a pipeline interpreter for a variable length instruction set

JS Yates, SC Root - US Patent 5,802,373, 1998 - [freepatentsonline.com](#)

... system 34 will, in general, translate nearly all ... instructions provided from the background translator 34, and ... program written for a non-native instruction set. ...

Cited by 15 - Related articles - Web Search - All 3 versions

UQBT: Adaptable Binary Translation at Low Cost

C Cluett, M Van Emmerik - COMPUTER, 2000 - [doi.ieeecomputersociety.org](#)

... than writing a handcrafted binary translator from scratch. ... translations to bytecodes (the instruction set of the ... uqbtbs and uqbtpb, to translate from Sparc and ...

Cited by 72 - Related articles - Web Search - BL Direct - All 7 versions

Guest Editors' Introduction: Welcome to the Opportunities of Binary Translation

ER Altman, D Kaeli, Y Sheffer - COMPUTER, 2000 - [doi.ieeecomputersociety.org](#)

... the software methods that let you translate at runtime ... user program to determine the minimal set of blocks ... function and inserts an inprof instruction in the ...

Cited by 57 - Related articles - Web Search - BL Direct - All 8 versions

FX! 32 a profile-directed binary translator

A Chernoff, M Herdeg, R Hookway, C Reeve, N Rubin, ... - Micro, IEEE, 1998 - [ieeexplore.ieee.org](#)  
... translator subsequently uses to translate executed x86 ... and stores, the translator  
coalesces increments ... expose the alternative instruction set architecture (ISA ...  
[Cited by 109](#) - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 8 versions](#)

### Dynamic and Transparent Binary Translation

M Gschwind, ER Altman, S Sathaye, P Ledak, D ... - COMPUTER, 2000 - [doi.ieeecomputersociety.org](#)  
... scheduling approach used by the BOA translator makes it ... BOA copies all registers  
to a set of backup ... Within a trace, BOA schedules instructions out of order and ...  
[Cited by 85](#) - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 8 versions](#)

### The design of a resourceable and retargetable binary translator

C Cifuentes, M Van Emmerik, N Ramsey - Reverse Engineering, 1999. Proceedings. Sixth Working ..., 1999 - [ieeexplore.ieee.org](#)  
... by us- ingknowledgeof the source-machine callingconventions and instruction set. ... HRT  
L to target-machine instructions, and it ... to identify and translate all code ...  
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Key authors: C Cifuentes - T Proebsting - E Altman - L Deutsch - J Yates

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